

ABSTRACT

A coding method suitable for use with ferroelectric or other nonvolatile counters subject to imprint ensures that all of the bits in the code are frequently switched and not left in a fixed data state. The general coding equation for this
5 method is such that: for an even integer n , it is represented by the conventional binary code of $n/2$; for an odd integer n , it is represented by the conventional binary code of the one's compliment of $(n - 1)/2$. Using the coding method of the present invention, every bit switches to its compliment when counting from an even number to an odd number. Since every bit switches at least once every
10 two counts, imprint is substantially reduced.